

PATENT

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Luca G. Fasoli, et al.

Title: MEMORY ARRAY INCORPORATING MEMORY CELLS ARRANGED
IN NAND STRINGS

Application No.: 10/729,843

Filed: December 5, 2003

Examiner: Luu, Pho M.

Group Art Unit: 2824

Atty. Docket No.: 023-0031

Confirmation No.: 8321

March 15, 2005

Mail Stop Amendment
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Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT
37 C.F.R. § 1.97(b)

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Pursuant to 37 C.F.R. § 1.56, § 1.97 and § 1.98, the undersigned brings to the Examiner's attention in the above-identified application the patents, publications, applications or other information identified in the attached:

- ☒ Form(s) PTO/SB/08A (5 page(s), with copy of 4 reference(s)).
- ☒ Form(s) PTO/SB/08B (2 page(s), with copy of 20 reference(s)).
- ☒ Listing of Pending Applications (1 page(s), with copy of no application(s)).
- ☐ Other: N/A

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For each item of information listed that is not in the English language, the undersigned has provided a concise explanation of the relevance through (i) an English language abstract, (ii) an English language equivalent application, or (iii) if cited in a search report or other action by a foreign patent office in a counterpart foreign application, an English language version of the search report or action that indicates the degree of relevance found by the foreign office.

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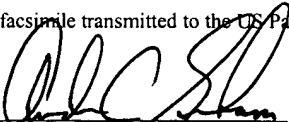
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- ☐ This Information Disclosure Statement is filed within three months of the filing date of a national application other than a continued prosecution application under § 1.53(d) or within three months of entry of the national stage as set forth in § 1.491 in an international application. Therefore, no fee is required.
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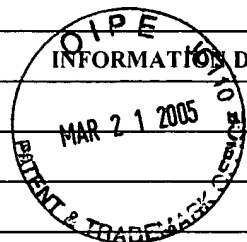
Respectfully submitted,



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PTO/SB/08A (Substitute for form 1449/PTO)		Attorney Docket No.: 023-0031
		Application No.: 10/729,843
INFORMATION DISCLOSURE STATEMENT BY APPLICANT		First Named Inventor: Luca G. Fasoli et al.
		Filing Date: December 5, 2003
		Group Art Unit: 2824
		Examiner Name: Pho M. Luu
Sheet 1 of 5		Date Submitted: March 15, 2005



U.S. PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	Document Number Number-Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	
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	AO	JA 62-155568	07-10-1987			
	AP	JA-2001-358237	12-26-2001			
	AQ	JA-2002-280467	09-27-2002			
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	BE					
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	BI	6,188,611 B1	02-13-2001	Endoh et al.	
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	BV					
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	BY					
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	BZ	6,515,888	02-04-2003	Johnson et al.	
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	CN					
	CO					
	CP					
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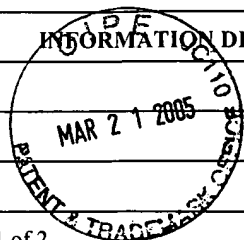
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	CQ	US-2002/0021587 A1.	02-21-2002	Sakui et al	
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	DD					
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NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of author (in CAPITAL LETTERS), title of article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	AA	DURISETY, CHANDRA SEKHAR ACHARYULU, "Analysis and Characterization of Single-Poly Floating Gate Devices in 0.35um PDSOI Process", A Thesis Presented for the Master of Science Degree, The University of Tennessee, Knoxville, December 2002, pp. i-viii and 1-80.	
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Examiner Signature		Date Considered	

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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	AO	SHIN, JONGSHIN, ET AL., "A New Charge Pump Without Degradation in Threshold Voltage Due to Body Effect," IEEE Journal of Solid-State Circuits, Vol. 35, No. 8, August 2000, pp. 1227-1230.	
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	AU		
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